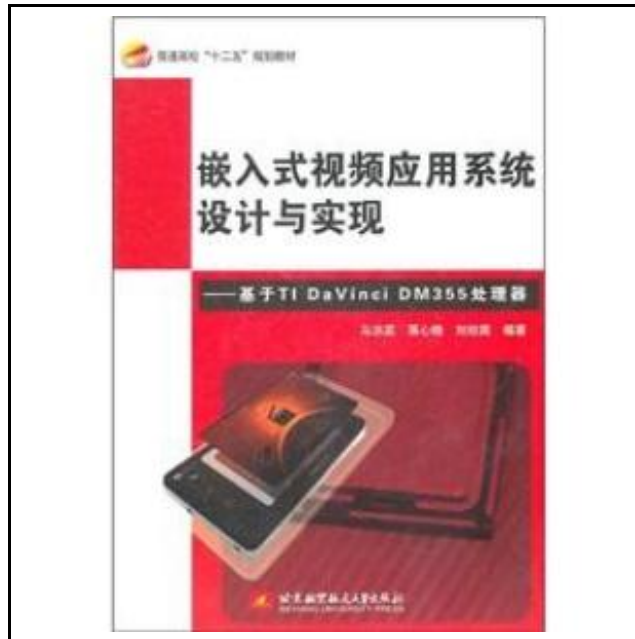


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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 397 Publisher: Beijing University of Aeronautics Pub. Date :2011-08-01 version 1. Ma Rui. Jiangxin Xiao. Liu Shaonan edited the embedded video application system design and implementation - based on TI DaVinci DM355 processor for TI DaVinci (Leonardo da Vinci) DM355 digital media processor and open source Linux operating system. about the DaVinci DM355 digital media processor architecture. resources and on-chip interface technology. the DM355 video processing system based on hardware design. Linux-based video processing during system initialization. drivers. and application design process. Embedded system design and implementation of video applications - based on TI DaVinci DM355 processor can be used as an embedded video system the direction of higher education of undergraduate and postgraduate teaching; also be related to the direction of R & D personnel as a reference. Contents: Chapter 1 Embedded System Design Overview Embedded 1.1-based embedded systems concepts 1.1.1 1.1.2 1.1.3 embedded system composition 1.1.4 embedded processor embedded operating system (OS) 1.2 embedded digital video system 1.3 embedded systems development trends in the digital video System Chapter 2 embedded ARM-based digital video system design 2.1 Overview 2.1.1 Leonardo DaVinci video processing technology. the advantages of video processing technology 2.1.2 Vinci DaVinci video processing technology 2.2 Overview 2.2.1 DaVinci video processor. video processor. video processor TMD320DM643x2.2.3 TMD320DM64462.2.2 Da Vinci Da Vinci DaVinci video processor. video processor TMD320DM3552.2.4 election type Guide 2.3ARM digital video embedded system design process in Chapter 3 DM355 DaVinci video processor architecture overview 3.1 Introduction 3.1.1DM355 3.1.3DM355 3.1.2DM355 block diagram overview of the ARM core 3.2ARM 3.2.1ARM nucleation 3.2.2ARM system 3.3.1 Introduction 3.3.2 Resource 3.3ARM nuclear status and mode of operation 3.3.3 Processor Status Register (PSR) 3.3.4 unusual and abnormal vector 3.3.516...



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